

	L #	Hit	S arch T xt	DBs
1	L1	3122	(361/760,782,783,784,785,786,790,803).ccls.	USP AT; US-P GPU B
2	L2	2507	(365/51,52,63,185.09).ccls.	USP AT; US-P GPU B
3	L3	605	(710/301,302).ccls.	USP AT; US-P GPU B
4	L4	951	326/30.ccls.	USP AT; US-P GPU B
5	L5	694	(257/664,728).ccls.	USP AT; US-P GPU B
6	L6	7782	1 2 3 4 5	USP AT; US-P GPU B
7	L7	618	6 and @pd>=20021201	USP AT; US-P GPU B

	L #	Hit	S arch T xt	DB
8	L8	143	7 and terminat\$3	USP AT; US-P GPU B
9	L9	104	8 and (impedance\$1 match\$3)	USP AT; US-P GPU B
10	L10	39	8 not 9	USP AT; US-P GPU B
11	L11	475	7 not 8	USP AT; US-P GPU B
12	L12	164	11 and (impedance\$1 match\$3)	USP AT; US-P GPU B
13	L13	26	7 and terminations	USP AT; US-P GPU B
14	L14	161	12 not 13	USP AT; US-P GPU B

	L #	Hit	S arch Text	DBs
15	L15	309	11 not (13 14)	USP AT; US-P GPU B
16	L16	617	361/777.ccls.	USP AT; US-P GPU B
17	L17	1491	174/261.ccls.	USP AT; US-P GPU B
18	L18	141	16 and terminat\$4	USP AT; US-P GPU B
19	L19	61	18 and (impedance\$1 match\$3)	USP AT; US-P GPU B
20	L20	80	18 not 19	USP AT; US-P GPU B
21	L21	286	17 and terminat\$4	USP AT; US-P GPU B

	L #	Hit	S ar h Text	DB
22	L22	104	21 and (impedance\$1 match\$3)	USP AT; US-P GPU B
23	L23	81	22 not (8 13 14 15 18)	USP AT; US-P GPU B
24	L24	182	21 not 22	USP AT; US-P GPU B
25	L25	334	7 and (edge long)	USP AT; US-P GPU B
26	L26	0	25 not (8 13 14 15 18 23 24)	USP AT; US-P GPU B
27	L27	4	("6545875" "6388886" "3644661" "6539449").pn.	USP AT; US-P GPU B
28	L28	2	27 and finger\$1	USP AT; US-P GPU B

	L #	Hit	S arch Text	DB
29	L29	2	28 and terminat\$4	USP AT; US-P GPU B
30	L30	2	29 and (impedance\$1 match\$3)	USP AT; US-P GPU B
31	L31	4	27 and edge	USP AT; US-P GPU B
32	L32	2	27 not 28	USP AT; US-P GPU B
33	L33	2	32 and edge	USP AT; US-P GPU B
34	L34	2	33 and terminat\$4	USP AT; US-P GPU B
35	L35	1	34 and (impedance\$1 match\$3)	USP AT; US-P GPU B

	L #	Hit	S arch T xt	DB
36	L36	1	34 not 35	USP AT; US-P GPU B